

## IN THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the Application:

### LISTING OF CLAIMS:

1. - 12. (Cancelled)

13. (Currently Amended) A method for manufacturing an area array package comprising:

coupling a grid array of primary electrical contacts to a coupling surface of a substrate within a central portion defined by the substrate, the grid array of primary electrical contacts configured to carry at least data signals between the area array package and a circuit board;

forming the primary electrical contacts of the grid array as a plurality of primary solder balls, each primary solder ball of the grid array defining a first diameter;

coupling a series of secondary electrical contacts to the coupling surface of the substrate within a peripheral area defined by the coupling surface, the series of secondary electrical contacts configured to carry power signals between the area array package and the circuit board, the series of secondary electrical contacts separate from the grid array; and

forming the series of secondary electrical contacts as a plurality of secondary solder balls, each secondary solder ball of the series defining a second diameter, the second diameter defined by each of the secondary solder balls being greater than the first diameter defined by each of the primary solder balls,

wherein coupling the grid array comprises coupling the grid array of primary electrical contacts to the coupling surface of a substrate defining at least one power plane, at least one ground plane, and at least one plated through hole

in communication with the at least one power plane and the at least one ground plane, the substrate further comprising a contact pad in electrical communication with the at least one plated through hole and configured to electrically couple with a secondary solder ball.

14. (Cancelled)

15. (Previously Presented) The method of claim 13 wherein the step of forming the series of secondary electrical contacts comprises:

placing at least two solder balls on a contact pad oriented within the peripheral area defined by the coupling surface, each solder ball defining a first diameter;

heating the at least two solder balls to cause the solder to undergo reflow;

forming a secondary solder ball on the contact pad, secondary solder ball defining a second diameter, the second diameter defined by the secondary solder ball being greater than the first diameter defined by each of the primary solder balls.

16. (Original) The method of claim 13 comprising coupling at least one power regulation device to the substrate and in electrical communication with the series of secondary electrical contacts.

17. (Previously Presented) The method of claim 13 comprising coupling the plurality of secondary solder balls to the substrate at a pitch of at least approximately 5 mm.

18. (Previously Presented) The method of claim 13 wherein the substrate defines a length of at least approximately 60 mm and a width of at least approximately 60 mm.

19. (Cancelled)

20. (Previously Presented) The method of claim 13 wherein the grid array of primary solder balls is configured in an array pattern of 50 columns having 50 primary solder balls per column.

21. - 28. (Cancelled)